


# ACKNOWLEDGEMENT RECEIPT

Electronic Version

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<b>Title of Invention</b>	<b>METHOD FOR FABRICATING INTEGRATED CIRCUITS HAVING BOTH HIGH VOLTAGE AND LOW VOLTAGE DEVICES</b>																																																																																																						
<div style="display: flex; justify-content: space-between;"> <div> <p>Submission Type :                      Utility Patent Filing</p> <p>Application Number:                      10/710616</p> <p>EFS ID:                                      65188</p> <p>Server Response:</p> </div> <div style="text-align: center;">  </div> </div>																																																																																																							
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